

IPL Seminar at EDS Fair Japan November 2012

Wednesday, November 14, 2012

11am - 11:45am

DM6-14-2

Interoperable Process Design Kits (iPDKs) Gain Momentum: Latest Update on iPDK Status and Challenges

Host: Kunihiko Tsuboi, STARC

Presenter:

Jingwen Yuan, IPL Alliance

Hiroshi Ishihara, TSMC

Atsushi Hirai, TowerJazz

Kunihiko Tsuboi, STARC

Description: "Interoperable PDKs (iPDKs) benefit the entire custom design ecosystem. Semiconductor foundries and IDMs create iPDKs to reduce their PDK development, validation, support and distribution costs while enabling advanced design flows and multiple EDA tool support. All leading foundries today are delivering iPDKs. Chip designers now enjoy access to best-in-class tools, interoperable flows, and improved productivity. In this session, presenters from multiple foundries and IPL Alliance member companies will highlight the benefits of iPDK standard and their experiences in developing and deploying foundry iPDKs. The IPL Alliance will present an update on the current and future IPL projects.

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