

NEWS RELEASE

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IPL Alliance Announces 3rd Annual Lunch Workshop at DAC on Tuesday July 28, 2009

Foundry Sponsor TSMC and Charter Sponsors Ciranova, Magma, Mentor Graphics, SpringSoft and Synopsys Present on Interoperable PDKs

MOUNTAIN VIEW, Calif. – July 16, 2009 – The Interoperable PDK Libraries (IPL) Alliance today announced that it will host a free lunch workshop at the Design Automation Conference (DAC) in San Francisco, California on Tuesday July 28, 2009. Entitled, “The EDA Earthquake: Interoperable PDKs Shakin’ Up the Analog Design World,” this is the 3rd year the IPL Alliance has hosted the workshop. This year, IPL Alliance members and customers will highlight key breakthroughs made since DAC 2008 in the development, validation, delivery, and usage of the interoperable PDK.

A complimentary hot buffet lunch will be served. Seating is limited. Please visit the IPL Alliance Web site at <http://www.iplnow.com/> to register.

IPL Lunch Workshop

The EDA Earthquake: Interoperable PDKs Shakin’ Up the Analog Design World

Foundry Sponsor: TSMC

Charter Sponsors: Ciranova, Magma, Mentor Graphics, SpringSoft and Synopsys

IPL Sponsors: AWR, Helic, JEDAT and Pyxis

Tuesday, July 28th
12:00pm – 1:30pm
San Francisco Marriott Hotel, Yerba Buena Salon 8

Last year’s IPL lunch featured the announcement of TSMC joining the IPL Alliance and collaboration with key alliance members to create the industry’s first 65-nanometer (nm) interoperable PDK. This year, IPL members will present a foundry-qualified and validated interoperable PDK. Attendees will hear about the challenges, features, roadmap and customer experiences using the kit.

12:00 – 12:10 Complimentary lunch buffet

12:10 – 12:20 Introduction & IPL overview and roadmap– IPL Alliance
12:20 – 12:35 Interoperable PDK implementation– Synopsys
12:35 – 12:55 TSMC interoperable PDK – TSMC
12:55 – 01:15 Using an interoperable PDK in a multi-vendor flow– Wipro
01:15 – 01:30 Q&A

Other IPL events at DAC include:

- IPL Alliance Demo
One PDK for ALL!
Synopsys Standards Booth #1016
Mon. to Wed. 9am to 6pm, Th. 9am to 1pm
For more information, visit: http://www.iplnow.com/ipldac_events.php

- DAC Pavilion Panel
Will Interoperable PDKs Fly in a Stodgy Analog World?
Tu. 3:30 p.m. to 4:15 p.m., DAC Booth #1928
For more information, visit: <http://www.dac.com/events/eventdetails.aspx?id=95-112>

- Interoperability Breakfast
Peace, Love & Interoperability: Improving Quality & Productivity with
Verification & Custom Design Standards
Wednesday, July 29 7:30 a.m. to 9:30 p.m. SF Marriott, Yerba Buena, Salon 7
For more information, visit:
<http://www.synopsys.com/Community/Interoperability/InteroperabilityBreakfast/Pages/default.aspx>

About the IPL Alliance

The IPL (Interoperable PDK Libraries) Alliance is an industry-wide collaborative effort to create and promote interoperable process design kit (PDK) standards. Since its inception in April 2007, the IPL Alliance has made tremendous progress in solving PCell interoperability. A free IPL reference library and an online interoperability demonstration are available for download at <http://www.iplnow.com>, the IPL Alliance web site. An IPL ecosystem has been established to validate PCell interoperability within all members' tools. The IPL Alliance has formed three technical working groups for Properties and Parameters, PCells and Constraints expanded its charters to address broader interoperability issues with foundry PDKs and design flows. IPL Alliance members include Applied Wave Research (AWR), Agilent Technologies, Ciranova, Helic, JEDAT, Magma Design Automation, Mephisto Design Automation, Micro Magic, Parallel

Engines, Pyxis, SpringSoft, SynCira, Synopsys, TSMC and Virage Logic. Mentor Graphics and Pulsic are supporting members.