Contact:
Jingwen Yuan
IPL Alliance
info@iplnow.com

SilTerra Joins the IPL Alliance

iPDKs of SilTerra's 130 nm Technology Now Available

MOUNTAN VIEW, Calif., October 28, 2015 – The Interoperable PDK Libraries (IPL) Alliance announced today that SilTerra Malaysia Sdn. Bhd., a Malaysian home-grown leading semiconductor wafer foundry, has joined the IPL Alliance, a standards organization whose charter is to establish an interoperable custom design ecosystem. Multiple SilTerra 130 nm Technology iPDKs (interoperable Process Design Kits) are now available for customers.

The IPL Alliance, which includes major electronic design automation (EDA), semiconductor and foundry companies, released the industry's first open standard for iPDKs in 2010. The iPDK standard is based on the OpenAccess database and uses standard languages and a unified architecture to enable interoperability among all EDA vendor tools. Engineering teams need to develop only a single PDK for each process node which reduces development costs, shortens design cycles and provides designers with earlier access to advanced process technologies across multiple tools.

"SilTerra is proud to be a new member of the IPL Alliance. With iPDK, we are committed to expanding our product portfolio to service design houses involved in the Internet of Things and power management related applications with the best technology and design environments," said Yit Loong Lai, Senior Vice President of Worldwide Sales and Marketing at SilTerra. "iPDKs are proven and provide significant benefits such as reducing PDK development and validation time, lowering support and distribution costs and enabling multiple EDA tool support. iPDKs provide our customers the flexibility to adopt proven tools for their analog/mixed-signal

designs."

"Standardization promotes openness, innovation and reuse," said Jingwen Yuan, president of the IPL Alliance and product marketing manager at Synopsys. "We welcome the addition of SilTerra to the IPL Alliance and look forward to the availability of additional iPDKs to meet the increasing demand from designers."

About SilTerra Malaysia Sdn. Bhd.

SilTerra Malaysia Sdn Bhd is a semiconductor wafer foundry offering fabrication and design support services in CMOS logic, High-Voltage, Mixed-signal, RF and MEMS technologies down to 90 nanometer feature size. SilTerra's wafer fab has a design-in capacity of 40,000 eight-inch wafers per month and currently serves customers in US, EU, Latin America, Taiwan, Korea and China.

Environmentally vigilant, SilTerra delivers award winning, world-class performance to its customers seeking flexible capacity, custom technologies, competitive advantages and around the clock customer support. SilTerra is ISO 9001:2008 and ISO 14001 certified. Starting commercial production in 2001, the company's headquarters and factory are located in Malaysia's Kulim High-Tech Park, with sales and marketing offices in San Jose (California) and Hsinchu (Taiwan).

For additional information on SilTerra or its services, please visit www.silterra.com

About the IPL Alliance

The IPL Alliance is an industry standards organization established to develop an interoperable ecosystem for custom design by creating and promoting interoperable PDK standards. There are currently more than 30 semiconductor and EDA company members. For more information, please visit the IPL Alliance web site at http://www.IPLnow.com or contact info@iplnow.com

###