

**For Immediate Release**

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**IPL ALLIANCE ANNOUNCES LUNCH WORKSHOP  
AT DESIGN AUTOMATION CONFERENCE ON MONDAY 9 JUNE 2008**

Foundry Sponsor TSMC and Charter Sponsors Ciranova, Magma, Silicon Canvas and Synopsys  
to Present an Interoperable PDK Mechanism

SANTA CLARA, Calif. – May 27, 2008 – The Interoperable PDK Library (IPL) Alliance announced today that it will host a free luncheon workshop at the Design Automation Conference in Anaheim, California on Monday June 9, 2008. Entitled, “Interoperable PDK Libraries: ‘The Proof is In the Pudding!’,” the workshop will explore the process and critical success factors of developing an interoperable PDK.

A complimentary hot buffet lunch will be served. Seating is limited and by reservation only. Please visit <http://www.iplnow.com/> to register.

Details:

**IPL Lunch Workshop  
Interoperable PDK Libraries: “The proof is in the pudding!”**

**Foundry Sponsor: TSMC  
Charter Sponsors: Ciranova, Magma, Silicon Canvas, Synopsys  
IPL Sponsors: AWR, Mentor Graphics**

Monday, June 9th  
12:00pm – 1:30pm  
Anaheim Convention Center, Ballroom D

At this lunch workshop, IPL members will present an interoperable PDK mechanism. Attendees will hear about the challenges, features, and roadmap, as well as first-hand user experience with the interoperable PDK.

12:00 – 12:20	Complimentary lunch buffet
12:20 – 12:40	Introduction & IPL Overview - Ed Lechner, Synopsys
12:40 - 12:55	TSMC’s View of Interoperable PDK - Tom Quan, TSMC
12:55 - 01:10	TSMC’s PDK Support Process - Steven Chen, TSMC
01:10 – 01:20	IPL Alliance Future Activities - Michael Ma, Ciranova
01:20 – 01:30	Q&A

Register now for this free event. Seating is limited and registration is required. Please visit <http://www.iplnow.com> to register.

Other IPL events at DAC include:

- The Synopsys Interoperability Breakfast on Wednesday June 11 from 7:30am to 9:30am.
- The IPL Alliance demo suite located in the Synopsys Standards Booth #1541.

Please visit the IPL Web site at <http://www.iplnow.com> to register for IPL DAC events.

### **About IPL Alliance**

The IPL (Interoperable PDK Libraries) Alliance is an industry wide collaborative effort to create and promote interoperable process design kit (PDK) standards. Since its inception in April 2007, the IPL Alliance has made tremendous progress in solving PCell interoperability. A free IPL reference library and an online interoperability demonstration are available for download at <http://www.iplnow.com>, the IPL Alliance web site. An IPL ecosystem has been established to validate PCell interoperability within all members' tools. The IPL Alliance has formed three technical working groups for Properties and Parameters, PCells and Constraints expanded its charters to address broader interoperability issues with foundry PDKs and design flows. IPL Alliance members include Applied Wave Research (AWR), Ciranova, Helic, JEDAT, Magma Design Automation, Micro Magic, Silicon Canvas, Silicon Navigator, Synopsys, TSMC and Virage Logic. Mentor Graphics and Pulsic are supporting members.

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