

NEWS RELEASE

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STARC Joins the IPL Alliance to Adopt iPDK Standard

*Japan's Leading Semiconductor Research Consortium Deploys
iPDK Methodology to Members*

MOUNTAIN VIEW, Calif. – April 21, 2010 – The Interoperable PDK Libraries (IPL) Alliance announced today that Japan's Semiconductor Technology Academic Research Center (STARC) has joined the Alliance to adopt Interoperable Process Design Kit (iPDK) standard for analog/mixed-signal designs. STARC has deployed a 90-nanometer (nm) reference iPDK to its member companies.

STARC, in collaboration with Synopsys (Nasdaq: SNPS), developed the STARC 90-nm mixed signal reference iPDK. Using STARC reference designs, the STARC iPDK has been validated with multiple OpenAccess-based flows including Synopsys' Galaxy Custom Designer™ solution and is currently in the validation process with additional custom design flows and tools. The validation process covers a comprehensive set of tests

including schematic capture, circuit simulation, layout, auto-routing, physical verification and extraction. The STARC iPDK and reference design flow is available now to STARC member companies.

“We are chartered by our member companies to investigate and recommend advanced tools, flows and technologies, such as interoperable process design kits, for modern analog and mixed signal designs,” said Kunihiko Tsuboi, senior manager, Mixed Signal Design Group, at STARC. “The IPL 1.0 standard promotes interoperability, IP reuse and reduces PDK development and support costs. Furthermore, it enables our member companies to have the flexibility to choose best-in-class tools for their advanced analog and mixed signal design flows.”

“In joining the IPL Alliance, STARC can help promote the PDK standard in Japan and accelerate the adoption of iPDKs with leading Japanese semiconductor manufacturers,” said Jingwen Yuan, president of the IPL Alliance and strategic alliance manager at Synopsys. “We look forward to the opportunity to extend the collaboration with STARC member companies so that they can realize the benefits of the iPDK.”

About STARC

Semiconductor Technology Academic Research Center, STARC, is a research consortium co-founded by major Japanese semiconductor companies in December 1995. STARC’s mission is to contribute to the growth of Japanese semiconductor industry by developing leading-edge SoC design technologies.

For more information, please visit <http://www.starc.jp/index-e.html>

About IPL Alliance

The IPL (Interoperable PDK Libraries) Alliance is an industry organization established to develop an interoperable eco-system for custom design by creating and promoting interoperable PDK standards. Current IPL Alliance members include Accelicon Technologies, Applied Wave Research (AWR), Agilent Technologies, Ciranova, GRID Simulation Technology, Helic, JEDAT, LFoundry, Magma Design Automation, Mephisto Design Automation, Micrologic Design Automation, Micro Magic, Parallel Engines, Pyxis, SpringSoft, SynCira, Synopsys, TowerJazz, TSMC and Virage Logic. Mentor Graphics and Pulsic are supporting members. For more information, please visit IPL Alliance web site at <http://www.iplnow.com> or contact info@iplnow.com

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